

深圳市矽源特科技有限公司

ShenZhen ChipSourceTek Technology Co. , Ltd.



HAA9007

Adaptive Charge-Pump, Receiver-Mode Class G Audio Amplifier

General Description

The HAA9007 is a high efficiency filter-less Class-G audio power amplifier with Automatic Gain Control technology and an adaptive high efficiency charge pump boost power supply. The device constantly monitors output power and dynamically adjust internal gain to prevent long time overstress across the speaker.

The adaptive charge pump structure activates automatically depending on the output signal. It generates 6V supply for output stage of amplifier when the peak output voltage is high. It can deliver 1W (THD+N=1%) of continuous average power to an 8 Ω load by a lithium/lon battery. The HAA9007 features high efficiency up to 81%, which helps extend battery life when playing audio.

The AGC features multi-level constant output power, which can Improve sound quality and suppress the clipping noise. It also helps designer to select suitable output power which match the speaker.

The HAA9007 has a 48µVrms low output noise at gain=8V/V to improve the signal to noise ratio (SNR).

The HAA9007 is available in small 1.57mmX1.61mm 14-ball WLCSP package with 400µm pitch.

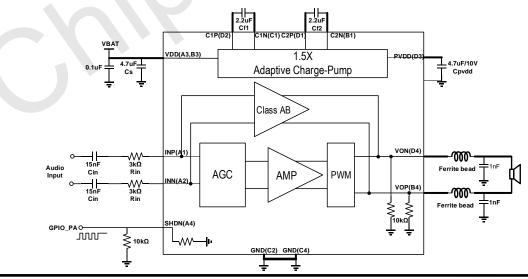
Simplified Application Diagram

Features

- Built-In AGC technology
- 4 Constant Output Power levels Control:
 1.2W ,1W ,0.8W ,0.6W@8Ω
- Adaptive Charge Pump Power Supply
- Low Output Noise : 48µVrms @ gain=8V/V
- Built-in Class AB Receiver mode
- SNR:98dB (3.8V,8Ω)
- High Efficiency : 81%
- Low quiescent current: 3.8mA (3.8V)
- 2.18W Output Power (8Ω @ 4.2V, THD+N=10%)
- THD+N:0.006%@1kHz,800mW,8Ω Load,3.8V Supply
- Thermal and Short-Circuit Protection with Auto Recovery
- Built-in Pop-and-click noise suppression
- Low RF Susceptibility
- Available in 1.57mmX1.61mm 14-ball WLCSP Package

Applications

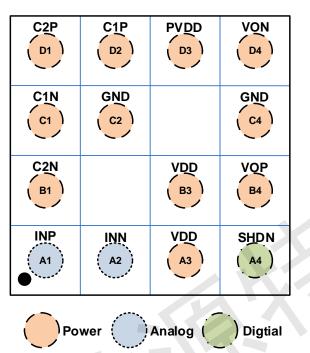
- Mobile Phones and Tablets
- Portable Media Players



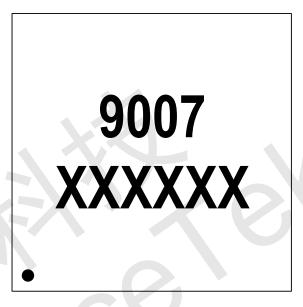
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Pin Configuration



HAA9007 TOP VIEW



Pin Functions

PIN No.	PIN Name	Description			
A1	INP	Positive audio input of the Class G Audio Amplifier			
A2	INN	Negative audio input of the Class G Audio Amplifier			
A3、B3	VDD	Supply voltage			
A4	SHDN	Single wire Pulse Control Terminal			
B1	C2N	Charge-Pump Flying Capacitor Terminal			
B4	VOP	Positive PWM audio Output of the Class G Audio Amplifier			
C1	C1N	Charge-Pump Flying Capacitor Terminal			
C2,C4	GND	Ground			
D1	C2P	Charge-Pump Flying Capacitor Terminal			
D2	C1P	Charge-Pump Flying Capacitor Terminal			
D3	PVDD	Audio power stage supply voltage			
D4	VON	Negative PWM audio Output of the Class G Audio Amplifier			

Order Information

Part No.	Package	Mark	Tape and Reel Information
HAA9007	CSP14L	9007 XXXXXX	3000/Reel



Absolute Maximum Ratings

Over operating free–air temperature range, TA= 25°C (unless otherwise noted)⁽¹⁾

Pa	Min	Max	Unit				
Supply Voltage	VDD	-0.3	5.5	V			
Input Voltage	INP,INN,SHDN		-0.3	VDD+0.3	V		
Operating free-air temperature range T _A			-40	85	°C		
Operating junction temperature range T _J			-40	150	°C		
Storage temperature range T_{STG}	-65	150	°C				
Minimum load impedance			3		Ω		
Human Body Model (HBM) ESD ⁽²⁾	2	V					
Thermal Metric							
θ_{JA} 14-ball WLCSP 1.57x1.		70	°C/W				

(1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

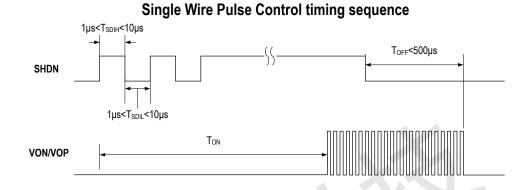
(2) This device series contains ESD protection and passes the following tests:

Human Body Model (HBM) standard: MIL-STD-883J/Method 3015.8 for all pins.

Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



Operating Control Description



Operation Mode Description

SHDN Signal	Mode	Description							
Control signal of SHDN PIN	Mode 1	AGC ON ,1.2W(8Ω) Output Power Control							
Mode1	Mode 2	AGC ON ,1.0W(8Ω) Output Power Control							
Mode2	Mode 3	AGC ON ,0.8W(8Ω) Output Power Control							
Mode3	Mode 4	AGC ON ,0.6W (8Ω)Output Power Control							
Mode4	Mode 5	Class AB Receiver Mode, Gain=1V/V							
Mode5	Mode6	Class AB Receiver Mode, Gain=3V/V							
	Mode7	AGC OFF							
Mode7									



Electrical Characteristics

VDD=3.6V, T_A = 25°C, R_L = 8 Ω +33 μ H, Rin = 3 $k\Omega$, Cin = 1 μ F (unless otherwise noted)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Supply Voltage Range	V _{DD}		3		5	V	
Shutdown Current	Isd			0.1	1	μA	
Turn Off Time	TOFF		100		500	μs	
Over Temp Protection	Tovp			155		°C	
Single wire pulse (SHDN PIN)	·		4			
High-level Input Voltage	V _{SDIH}		1.3		Vdd	V	
Low-level Input Voltage	Vsdil		0		0.35	V	
High-level Duration	T _{SDIH}		1		10	μs	
Low-level Duration	TSDIL		1		10	μs	
Single wire pulse (SEL PIN)							
High-level Input Voltage	V_{SEL}		1.3		V _{DD}	V	
Low-level Input Voltage	V_{SEL}		0		0.35	V	
Charge-Pump(CP) Boost Cor	nverter						
Active Threshold	Vat			0.7		V	
Over Voltage Production	VOVP		5.7	6	6.3	V	
		VOP-VON _{peak} < V _{AT}		V _{DD}		V	
Output Regulation Voltage	Pvdds	VOP-VON peak> VAT, VDD*1.5 < VOVP		1.5*V _{DD}		V	
		VOP-VON peak> VAT VDD*1.5 > VOVP		Vovp		V	
Switching Frequency	FCP			1.06		MHz	
CP ON Resistance	RONCP	V _{DD} = 3.8V, lout = 0.9A		1.2		Ω	
Class D Power Amplifier							
Operating Quiescent Current	lq	Input AC Ground		3.6		mA	
Turn-on Time	Ton			41.		ms	
Output Offset Voltage	Vos	Input AC Ground	-20		20	mV	
Switching Frequency	Fpa			800		kHz	
Voltage Gain	Av			16.6		V/V	
Input Impedance	Rin	Speaker Mode		16.6		kΩ	
Frequency Response of Gain		BW = 20Hz to 20kHz	-0.3		0.3	dB	
Output Naiss Valtage	Ma	Rin = $3k\Omega$, Cin = $33nF$, Gain = 16, A-weighted		65		μVrms	
Output Noise Voltage	V _N	Rin = $23k\Omega$, Cin = $33nF$, Gain=8, A-weighted		48			
Output Impedance in SD	Zo	SHDN = GND		10k		Ω	
Total Harmonia Distortion	THD+N	$V_{DD} = 3.8V, P_0 = 0.3W, R_L = 8\Omega + 33\mu H, Mode7$ 0.0					
Total Harmonic Distortion Plus Noise		V_{DD} = 3.8V, P ₀ = 0.8W, R _L = 8 Ω +33µH, Mode7		0.01		%	
FIUS NUISE		V_{DD} = 3.8V, P ₀ = 1 .2W, R _L = 8 Ω +33 μ H, Mode7		0.011			
	~	$V_{DD} = 4.2V, P_0 = 1 W, R_L = 8\Omega + 33\mu H$		79		0/	
Class G + CP Efficiency	η	V _{DD} = 3.8V, P _O = 0.8 W, R _L = 8Ω+33μH		81		%	



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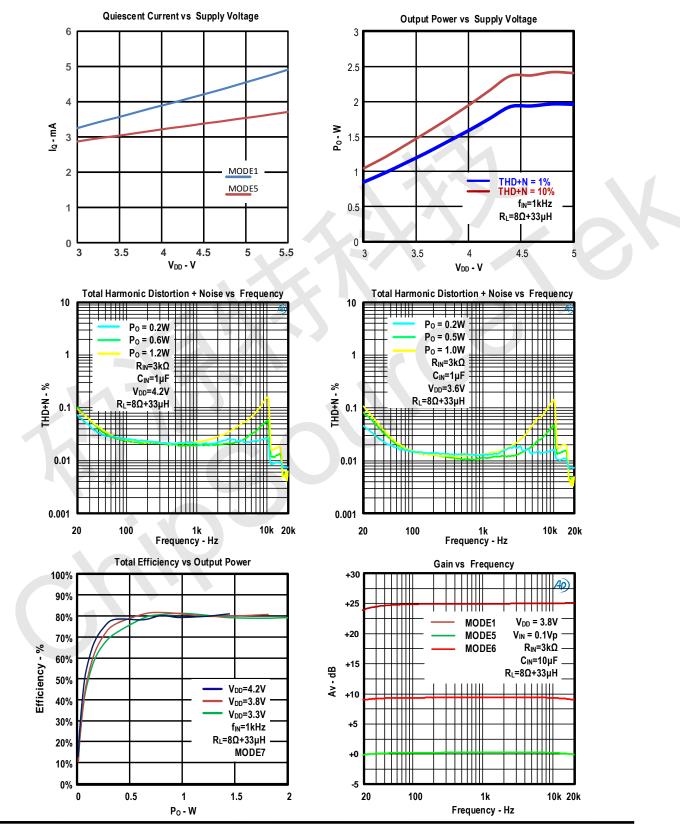
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		V _{DD} = 3	.3V, P _O = 0.6 W, R _L = 8Ω+3	3µH			80			
							-74			
	PSRR	ln In	put AC Ground, V _{ripple} =	1kHz			-70		dB	
Rejection			200mVpp,				-56			
		V _{DD} = 4	V _{DD} = 4.2V, THD+N = 1%, R _L = 8Ω+33μH				1.765			
		V _{DD} = 3	V _{DD} = 3.8V, THD+N = 1%, R _L = 8Ω+33μH				1.42			
		V _{DD} = 3	V_{DD} = 3.3V, THD+N = 1%, R _L = 8 Ω +33 μ H				1.05			
		$V_{DD} = 4$	$V_{DD} = 4.2V, THD+N = 10\%, R_{L} = 8\Omega+33\mu H$ $V_{DD} = 3.8V, THD+N = 10\%, R_{L} = 8\Omega+33\mu H$				2.181			
Output Davies		V _{DD} = 3					1.743		w	
Output Power	Po	V _{DD} = 3	V _{DD} = 3.3V, THD+N = 10%, R _L = 8Ω+33μH				1.28	8		
		$V_{DD} = 4$.2V, THD+N = 1%, RL= 4Ω+	+33µH			2.351			
		$V_{DD} = 4$.2V, THD+N = 10%, RL= 4Ω	2+33μH			2.765			
		$V_{DD} = 4$.2V, THD+N = 1%, RL= 3Ω+	+33µH			2.31			
		$V_{DD} = 4$.2V, THD+N = 10%, RL= 3Ω	2+33μH			2.59			
		Mode1,	R∟= 8Ω+33μH				1.2			
Operating Quiescent Curre Turn-on Time Output Offset Voltage Voltage Gain Input Impedance Output Noise Voltage		Mode2,	R∟= 8Ω+33μH				1		14/	
	Po	Mode3,	Mode3, R _L = 8Ω+33μH				0.8		W	
		Mode4,	Mode4, R _L = 8Ω+33μH				0.6	1.6		
AGC Attack Time	Татк						41		ms	
AGC Release Time	Trel						1.15		S	
Max Attenuation Gain						,	-13.5		dB	
Signal Noise Ratio	SNR	V _{DD} = 3	.8V ,Po = 1.42W, R _L = 8Ω+	33µH			98		dB	
Class-AB Receiver Power An	nplifier								•	
Operating Quiescent Curre	ent	Iqab	Input AC Ground, V _{DD} = 3	3.6V			3		mA	
Turn-on Time		Tonab	Receiver Mode				41		ms	
Output Offset Voltage		Vos	Input AC Ground			-8		8	mV	
Valtara Cain		Δ	Mode5			1				
voltage Gain		Av	Mode6				3		V/V	
Input Impedance		D	Mode5				45k		0	
		RINI	Mode6			45k		Ω		
		1/	C _{IN} = 33nF, A-weighted, Mode5			8.5				
Output Noise Voltage		VN	C _{IN} = 33nF, A-weighted, Mode6			18.5		μVrms		
Total Harmonic Distortion Plus Noise			V_{DD} = 4.2V, P_0 = 10mW, R_L = 8 Ω , f = 1kHz			0.21		%		
		THD+N	V_{DD} = 4.2V, P_{O} = 50mW, R_{L} = 8 $\Omega,$ f = 1kHz			0.15		%		
			V _{DD} = 4.2V, P _O = 100mW	$V_{DD} = 4.2V, P_0 = 100 \text{mW}, R_L = 8\Omega, f = 1 \text{kHz}$			0.12		%	
Dowor Supply Dipolo Doiog	or Supply Pipple Poiestion	DCDD	Mode5, Input AC Ground	,	217Hz		-100		ЧD	
rower supply kipple kejec	UUH	PSRR	V_{ripple} = 200mVpp, V_{DD} = 4	4.2V	1kHz		-98		dB	
Signal Noise Ratio		SNR	V _{DD} = 3.8V ,Po = 100mW	$R_L = 8\Omega +$	33µH		104		dB	

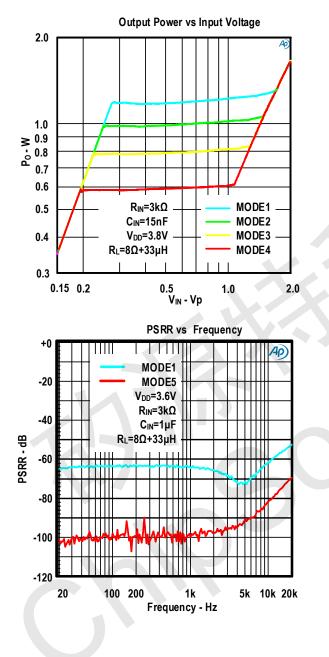


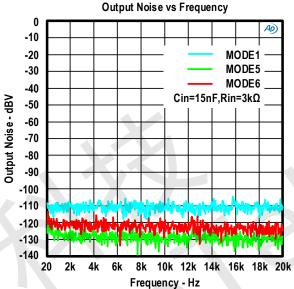
Typical Characteristics



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Application Information

Decoupling Capacitor (Cs)

The HAA9007 is a high-performance Class-G audio amplifier that requires adequate power supply decoupling to ensure the efficiency is high and total harmonic distortion (THD) is low. For higher frequency transients, spikes, or digital hash on the line, a good low equivalent-series-resistance (ESR) 4.7μ F ceramic capacitor placed as close as possible to the device V_{DD} pin works best. Placing this decoupling capacitor close to the HAA9007 is important for the efficiency of the Class-G amplifier, because any resistance or inductance in the trace between the device and the capacitor can cause a loss in efficiency. For filtering higher-frequency noise signals, a 0.1μ F capacitor placed near the audio power amplifier would also help.

Charge Pump Flying Capacitor (Cf1, Cf2)

The flying capacitor affects the load regulation and output impedance of the charge pump. The value of flying capacitor is too small results in a loss of current drive, leading to a loss of amplifier headroom. A higher valued flying capacitor improves load regulation and lowers charge pump output impedance to an extent. Selecting a 2.2µF/6.3V will help regarding the load regulation and the device's ability to provide sufficient current drive.

Charge Pump Hold Capacitor (CPVDD)

The value and ESR of the hold capacitor (C_{PVDD}) directly affects the ripple on C_{PVDD} . Increasing the value of C_{PVDD} reduces output ripple. Decreasing the ESR of C_{PVDD} reduces both output ripple and charge pump output impedance. A 4.7uF/10V capacitor is recommended.

Beam Filters

A ferrite bead filter can often be used if the design is failing radiated emissions without an LC filter and the frequency sensitive circuit is greater than 1MHz. This filter functions well for circuits that just have to pass FCC and CE because FCC and CE only test radiated emissions greater than 30MHz. When choosing a ferrite bead, choose one with high impedance at high frequencies, and low impedance at low frequencies. In addition, select a ferrite bead with adequate current rating to prevent distortion of the output signal. Use an LC output filter if there are low frequency (<1MHz) EMI sensitive circuits and/or there are long leads from amplifier to speaker. When use filter, it should be placed as close as possible to the device VOP/VON pin.

Input Resistors (R_{IN})

The HAA9007 has internal input resistors of $16.6k\Omega$. The input resistors set the gain of the amplifier according to equation below.

$$Gain = \frac{320k\Omega}{R_{IN} + 16.6k\Omega}$$

Place the input resistors very close to INN/INP pin to limit noise injection on the high-impedance nodes.

Input Capacitors (CIN)

The input capacitors and input resistors form a high-pass filter with the corner frequency fo, determined in equation below.

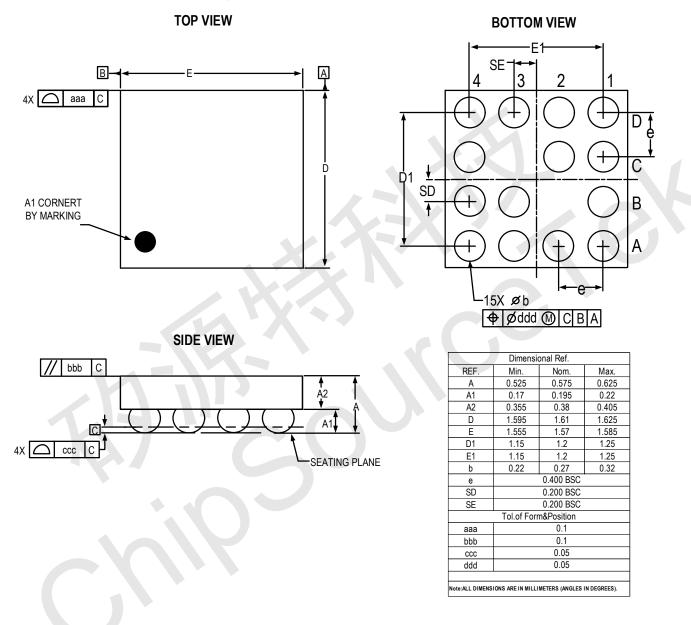
$$f_o = \frac{1}{2\pi * C_{\rm IN} * (R_{\rm IN} + 16.6 \mathrm{k}\Omega)}$$

The value of the input capacitor is important to consider as it directly affects the bass (low frequency) performance of the circuit. Speakers in wireless phones cannot usually respond well to low frequencies, so the corner frequency can be set to block low frequencies in this application. If the corner frequency is within the audio band, the capacitors should have a tolerance of $\pm 10\%$ or better, because any mismatch in capacitance causes an impedance mismatch at the corner frequency and below, it may cause turn-on pop noise.



Package Outline

1.57mmX1.61mm 14-ball WLCSP Package



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